Atty. Ref.: Aries-35

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

William Y. Sinclair

Serial No.

10/602,480

Filed

June 23, 2003

For

INTEGRATED CIRCUIT TEST PROBE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached hereto is a copy of Form PTO-1449 which identifies several prior art references. Copies of the identified references are attached as well. These references include a patent that has issued recently to the assignee of the subject invention as well as patents which are believed to be of general relevance in that they show prior art integrated circuit test probes including spring members.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

on October 29, 2003

Janis van Akelijen

J. VAN A KELIJEN

The Examiner is requested to consider this prior art during the examination and to make the references of record. Early and favorable action is solicited.

Respectfully submitted,

Anthony J. Casella, Esq.

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Date: 10/29/03

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			*** 1C35	Filing Date June 23, 2003			Group Art Unit 2858		
-		B UCI 3 T	1333 A	U.S. PAT	ENT DOCUMENTS				
EXAMINER INITIAL	REF	DOCUMENT NUMBER BADE	MARY DATE		NAME	CLASS	SUBCLASS FILING DATE IF APPROPRIATE		
		4,783,624	11-1988	Sabin Giringer et al.					
		4,918,384	4-1990						
		5,009,613	4-1991	Langga	rd et al.				
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	REF	DOCUMENT NUMBER	DATE		COUNTRY		SUBCLASS	Trans YES	NO NO
									
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				OTHER	DOCUMENTS (Includin	g Author, Title, L	Pate, Pertinent Pa	ges, Etc.)	
EXAMINER					DATE CONSIDERED			·	
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Docket Number (Optional)

Application Number